

**(19) World Intellectual Property Organization
International Bureau**



(43) International Publication Date
19 April 2001 (19.04.2001)

PCT

(10) International Publication Number
WO 01/27676 A1

- (51) **International Patent Classification⁷:** **G02B 6/42**

(21) **International Application Number:** PCT/US00/28086

(22) **International Filing Date:** 11 October 2000 (11.10.2000)

(25) **Filing Language:** English

(26) **Publication Language:** English

(30) **Priority Data:**
09/418,022 14 October 1999 (14.10.1999) US

(71) **Applicant (for all designated States except US):** **DIGITAL OPTICS CORPORATION** [US/US]; Suite J, 5900 Northwoods Business Parkway, Charlotte, NC 28269 (US).

(72) **Inventors; and**

(75) **Inventors/Applicants (for US only):** **HAN, Hongtao** [US/US]; 204 Chandeleur Drive, Mooresville, NC 28117 (US). **FELDMAN, Michael, R.** [US/US]; 3117 Lakewood Edge, Charlotte, NC 28269 (US).

(81) **Designated States (national):** AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

(84) **Designated States (regional):** ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:
— *With international search report.*

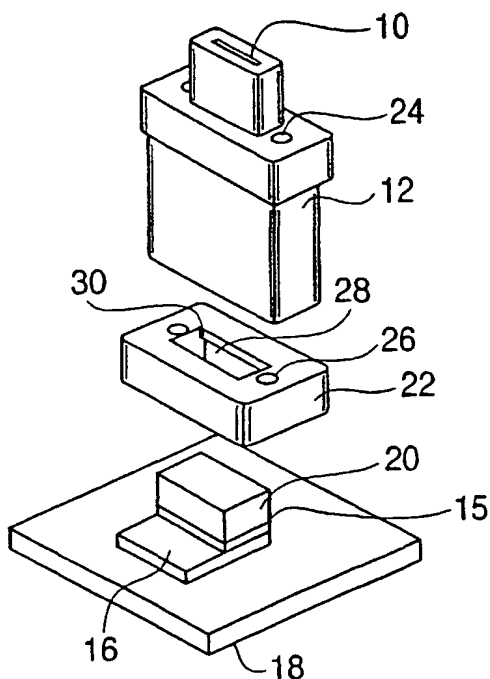
For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

— *With international search report.*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: OPTICAL SUBASSEMBLY



(57) Abstract: An optical subassembly includes an opto-electronic device (14), an optics block (20) and a spacer (15), separate from the optics block (20) and providing spacing between the opto-electronic device (14) and the optics block (20). The opto-electronic device (14), the optics block (20) and the spacer (15) are aligned and bonded together. This subassembly is particularly useful when coupling light between the opto-electronic device (14), and a fiber (10). The optical subassembly may also include an opto-electronic device (14), an optics block (20) and a sealing structure surrounding the opto-electronic device (14). The opto-electronic device (14), the optics block (20) and the sealing structure are aligned and bonded together.

WO 01/27676 A1

OPTICAL SUBASSEMBLY

Field of the Invention

The present invention is directed to an optical subassembly with fibers, particularly for use in fiber communication systems.

Description of Related Art

5 There are numerous ways to couple light to and from opto-electronic devices and fibers. One typical manner in which this is achieved is to butt couple the opto-electronic devices right up against the end faces of the fiber. Such butt-coupling requires active alignment to achieve desired levels of coupling efficiency. Further, butt-coupling does not allow the light beam to be modified. Finally, such butt-coupling typically requires
10 close positioning of the opto-electronic devices in accordance with the spacing of the fibers, increasing crosstalk.

Another manner of achieving coupling between fibers and opto-electronic devices is to use short fibers, which in turn are coupled to the fibers. This allows surface emitting opto-electronic devices to be coupled with fibers, but still requires active alignment.

15 One passive alignment scheme proposed involves providing holes in all of the components to be aligned, e.g., a ferrule housing the fibers, a light coupling device including optics and a substrate including the opto-electronic devices. Pins are then inserted into the holes to realize alignment of all the elements. Such single shot alignment

-2-

may not be accurate enough for all applications. Further, the materials which can be used for the light coupling device are limited when the holes need to be provided therein. Finally, such alignment requires that there be a linear relationship among all of the components.

5

SUMMARY OF THE PRESENT INVENTION

The present invention is therefore directed to an optical subassembly which substantially overcomes one or more of the problems due to the limitations and disadvantages of the related art.

10

This and other advantages may be realized by providing an integrated active optical system including an opto-electronic device, an optics block, and a spacer, separate from the optics block, providing spacing between the opto-electronic device and the optics block, the opto-electronic device, the optics block and the spacer being aligned and bonded together.

15

The spacer and the optics block may be aligned and bonded on a wafer level prior to dicing. The spacer may surround the opto-electronic device. The system may further include a substrate, both a bottom of the opto-electronic device and the spacer being bonded to the substrate. The opto-electronic device may be bonded to the substrate on a wafer level prior to dicing. A top surface of the opto-electronic device may be bonded to the spacer and the spacer further include interconnection tracks. The opto-electronic device may be bonded to the spacer on a wafer level prior to dicing. The substrate, the

20

-3-

optics block and the spacer may all be made of silicon. The system may be surface mounted to an electrical interface.

The opto-electronic device may include at least two opto-electronic devices. The at least two opto-electronic devices may be a light source and a light detector or may be an array of identical opto-electronic devices.

The above and other objects may further be realized by providing a system including a housing having a fiber, an opto-electronic device, and an optics block having two surfaces, each surface having an optical element thereon, the optics block coupling light between the opto-electronic device and the fiber, the housing, the opto-electronic device and the optics block being integrated together.

The opto-electronic device may include at least two opto-electronic devices and the fiber may include at least two fibers. The at least two opto-electronic devices may be separated from each other in at least one direction by more than the at least two fibers are separated from one another. The at least two opto-electronic devices may be separated from each other in at least two directions by more than the at least two fibers are separated from one another in each respective direction. The at least two opto-electronic devices may include a light source and a light detector or an array of identical opto-electronic devices.

The system may further include a spacer block between the optics block and the opto-electronic device. The system may further include a substrate, both a bottom of the opto-electronic device and the spacer being bonded to the substrate. A top surface of the

-4-

opto-electronic device may be bonded to the spacer and the spacer may further include interconnection tracks.

The optical axis of the fiber and the optical axis of the opto-electronic device may be at an angle. The system may further include a reflective surface directing light between the opto-electronic device and the fiber. At least one optical element on the optics block may homogenize light. The system may be surface mounted to an electrical interface. The optical elements on the optics block may be made on a wafer level.

The above and other objects of the present invention may be realized by providing a system including an opto-electronic device, an optics block having at least one optical element thereon, and a sealing structure surrounding the opto-electronic device, the opto-electronic device, the optics block and the sealing structure being integrated.

The system may be surface mounted to an electrical interface. The system may include a spacer between the optics block and the opto-electronic device. The sealing structure may include a bottom surface of the optics block and the spacer. An optical element may be provided on the spacer.

The system may include a substrate receiving the opto-electronic device therein. The sealing structure may include the substrate and a bottom surface of the optics block. The system may include a spacer block between the optics block and the opto-electronic device. The sealing structure may include the substrate and a bottom surface of the spacer. The substrate may have vias there through connecting the opto-electronic device to other systems. The opto-electronic device may be in contact with the substrate, the substrate

-5-

serving as a heat sink for the opto-electronic device.

These and other objects of the present invention will become more readily apparent from the detailed description given hereinafter. However, it should be understood that the detailed description and specific examples, while indicating the preferred embodiments of the invention, are given by way of illustration only, since
5 various changes and modifications within the spirit and scope of the invention will become apparent to those skilled in the art from this detailed description.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, aspects and advantages will be described with
10 reference to the drawings, in which:

Figure 1A is an exploded elevational perspective view of an interface of the present invention in conjunction with the fibers in a housing and the opto-electronic devices;

Figure 1B is an elevational perspective view of the system of Figure 1A;

15 Figure 1C is a side view also illustrating internal features of the system of Figure 1B;

Figure 1D is an exploded front view also illustrating internal features of the system of Figure 1B;

Figure 1E is a top view of the system of Figure 1B;

20 Figure 1F is a front view of the system of Figure 1B;

-6-

Figure 2A is an exploded elevational perspective view of an optical subassembly of the present invention;

Figure 2B is an exploded side view of Figure 2A;

Figure 3A is an exploded perspective view of the fiber housing and an interface
5 of the present invention;

Figure 3B is an exploded side view of Figure 3A;

Figure 4A is a front view of another embodiment of the optical interface of the present invention;

Figure 4B is top view of the opto-electronic devices in relation to alignment holes;

10 Figure 5 is a cross-sectional side view of another embodiment of the interface of the present invention;

Figure 6 is a cross-sectional side view of another embodiment of the optical subassembly of the present invention;

Figure 7A is an elevational exploded view of another embodiment of the optical
15 subassembly of the present invention; and

Figure 7B is an exploded side view of the configuration shown in Figure 7A.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

As can be seen in Figures 1A-1F, a plurality of fibers 10 are inserted into a ferrule
12. Opto-electronic devices 14 which are to be in communication with the fibers 10 are
20 preferably provided on a silicon bench or sub-mount 16. In turn, this silicon bench 16 is

-7-

preferably provided on a substrate 18. An optics block 20 provides at least one optical element between each opto-electronic device 14 and a corresponding fiber 10. The optics block 20 is preferably spaced from the opto-electronic devices 14 by a spacer 15. The optical elements preferably include elements which collimate, focus and/or homogenize the light. Since the optics block has two surfaces, two optical elements may be provided thereon. Further, if required, additional optics blocks may be bonded to and spaced from the optics block 20 to provide additional surfaces.

A mechanical interface 22 aligns the optics block 20, which is already aligned with the optical devices 14 and with the mechanical interface 22, with the fibers 10. This may be achieved by alignment features on both the mechanical interface 22 and the ferrule 12 housing the fibers 10. In the particular example shown, these alignment features consist of holes 24 in the ferrule 12, which are already typically present for aligning the ferrule with other devices, and alignment holes 26 in the mechanical interface 22. Once these alignment holes 24, 26 are aligned, an alignment pin, not shown, may then be inserted therein to maintain the aligned position.

The provision of separate elements to provide the mechanical interface and the optical interface provides several advantages. For example, the provision of the alignment holes 26 in the mechanical interface 22 allows the optics block to be made of a material selected for its optical properties. For example, the optics block may be made of glass, which is preferable for forming optics therein. However, it is difficult to accurately form cylindrical holes in glass. Thus, this material would not be suitable if the

-8-

holes had to be provided therein as well, i.e., if the mechanical and optical interface were to be realized by single element. Further, since the mechanical interface is to accept the alignment pins, it must be of sufficient size to accommodate the pins. Glass may be too fragile for such a purpose. Finally, glass is able to withstand the heat such as during soldering of the device to a printed circuit board or other electrical interface. Thus, the system may be surface mounted or pluggable to an electrical interface.

The mechanical interface may similarly be made of a material best suited for its function. The mechanical interface 22 also preferably includes an aperture 28 which allows light to travel between the opto-electronic devices 14 and the fibers 10 without interference from the mechanical interface. This aperture also allows the mechanical interface to be made of any desired material, such as an opaque, thermally stable material in which holes may be accurately and easily formed, such as a glass filled plastic, ceramic or molded plastic, without regard to the optical properties thereof.

Further, in the particular example shown, the aperture 28 is made large enough to surround the optics block 20, except for at a lip 30, which in turn provides the desired separation between a top of the optics block 20 and an end face of the fibers. If the mechanical interface 22 is made of a material which is transparent to wavelengths of light being exchanged between the fibers and the opto-electronic devices, such an aperture 28 may no longer be needed. Some cut-out for accepting the optics block 20, with the remaining portion serving as a spacer, may still be desirable. Either configuration will result in no physical contact between the fibers 10 and the optics block 20.

-9-

The alignment for the entire structure is discussed below in relation to Figures 2A-3. follows. Figures 2A-2B, show the alignment of the optics subassembly including the optics block 20 and the opto-electronic devices 14. First, the opto-electronic devices 14 are provided on the bench 16. Then, if the spacer 15 is being used, alignment features 34, such as fiducial marks, on the spacer 15 are aligned to alignment features 32, such as fiducial marks, on the bench 16. The spacer 15 is then bonded, e.g., using solder or epoxy, into place on the bench 16. The bevels which can be seen on the interior surface of the spacer 15 simply arise when using silicon as the spacer and the hole therein is formed by wet etching silicon along its crystalline plane. While wet-etching is a simple way of forming the hole in the spacer, vertical side walls may be more advantageous, e.g., for load bearing. Substantially vertical side walls may be realized by dry etching silicon. Further, other materials such as ceramic, glass, plastic, may be used for the spacer 15. If the spacer 15 is transparent to wavelengths of interest, the hole therein may not be required.

Then, alignment features 36, such as fiducial marks, on the optics block 20 are aligned with the corresponding features on the spacer 15 and the bench 16 to align the optics block to the opto-electronic devices 14. The optics block 20 is then bonded into place, e.g., using solder or epoxy, on the spacer 15. The optical elements on the optics block 20, as well as the alignment features 36, may be mass-produced on a wafer level and then diced to form individual optics blocks. Thus, only the alignment of the optical block 20 is required to align all of the optical elements thereon with the opto-electronic

SUBSTITUTE SHEET (RULE 26)

-10-

devices 14.

Preferably, the alignment and bonding of the spacer and the optics block occur on a wafer level, and then diced to form respective dies which are then aligned to the bench 16. The alignment of the spacer is not very sensitive, i.e., the spacer just needs to be aligned so that it does not block light between the optics block 20 and the opto-electronic device. While a spacer may be formed directly on the optics block itself, the use of a separate spacer 15 allows larger vertical separation to be achieved. The use of a separate spacer is particularly advantageous when providing optical elements on a bottom surface of the optics block 20, since the processes for forming the optics and the spacer features interfere with each other. Finally, use of a separate spacer allows the sealing off of the opto-electronic device 14 to be more readily and stably achieved. Such sealing protects the opto-electronic device 14 from environmental factors, such as humidity.

For certain wavelengths, e.g., in the near infrared, the optics block 20 may be made of another material, e.g., silicon. Then, all of the elements in the optical subassembly, i.e., the substrate, the spacer and the optics block, may be made of the same material, e.g., silicon. Making all of these elements of the same material reduces stress between these elements due to a difference in the thermal coefficient of expansion.

Alignment of the optics block 20 to the mechanical interface 22 and the fibers 10 is shown in Figures 3A-3B. While the optics block 20 has already been aligned with the opto-electronic devices 14, only the optics block 20 is shown for simplicity. In the particular example shown, the optics block 20 is to be passively aligned with the

-11-

mechanical interface 22. Access holes 38 are provided in the mechanical interface to facilitate positioning of the optics block 20. When the mechanical interface is not to surround the optics block, the access holes 38 are not needed.

Such passive alignment may be realized using fiducial marks and/or mechanical mating features on the optics block 20 and the lip 30 of the mechanical interface 22. The lip 30 provides an optical mounting surface which maintains the optics block 20 at the desired distance from the end face of the fibers 10. Once aligned, the optics block 20, and thus the opto-electronic devices 14, are bonded to the mechanical interface 22. The mechanical interface 22, and all the components bonded thereto, are aligned to the housing 12 via alignment holes 24, 26 to complete the structure.

In addition to the passive alignment set forth above, in which alignment features are provided on the elements being aligned, passive alignment may also be realized using an alignment template and/or using the position of the holes for receiving the pins in the mechanical interface. Further, active alignment may also be used.

An alternative embodiment is shown in Figure 4A. Here, the mechanical interface 22 does not surround the optics block, but rather is positioned on top of the optics block 20. The aperture 28 and the alignment holes 26 are still part of the mechanical interface 22, but the other features are not needed. Further, the alignment features may be included on the body of the mechanical interface 22, since the lip is no longer present.

By utilizing both surfaces of the optics block 20, the opto-electronic devices 14 may be placed further apart, while still realizing a compact system for delivering light

SUBSTITUTE SHEET (RULE 26)

-12-

between the opto-electronic devices 14 and the fibers 10. Such placement may reduce cross talk between the opto-electronic devices. As shown in Figure 4A, assuming the opto-electronic devices are light emitters, optics 44 on a first surface 42 of the optics block 20 collimates and deflects light from the opto-electronic device 14. Optics 46 on a second surface 48 of the optics block 20 focuses light onto the fiber 10. Obviously, if the opto-electronic devices are detectors, the functioning of the optics would be reversed.

The ability to place the opto-electronic devices further apart than the fibers is particularly advantageous when the system is a transceiver system, i.e., there is at least one light emitter and at least one light detector. This spacing may be further enhanced by additionally separating the emitter and detector in a direction orthogonal to the direction shown in Figure 4A. Such a configuration is shown in Figure 4B, where a light emitter 50 is separated from a light detector 52 in two directions. While these elements are still between the alignment holes 24, 26, they are further apart than the fibers 10 and are also separated in an orthogonal direction. Such separation minimizes crosstalk, while maintaining the original profile. Further, this separation can be realized even when the optics block is not larger than the mechanical interface.

A configuration employing the interface of the present invention where the fiber housing is positioned orthogonally to the plane of the opto-electronic devices is shown in Figure 5. The alignment holes 24, 26 are still used to align the fiber housing 12 and the mechanical interface 22, the mechanical interface 22 is now aligned to the side of the optics block 20. In order to direct light between the fibers and the opto-electronic devices

-13-

14, a reflective surface 60 is provided. As shown in Figure 5, this reflective surface 60 may be formed in glass or other materials. A metal coating may be provided on this surface to enhance the reflectivity thereof. The material having the reflective surface may then be bonded to a top surface of the optical block 20.

5 In the particular example shown in Figure 5, the opto-electronic element 14 is a VCSEL and another opto-electronic element 14' is a power monitor for monitoring the power output by the VCSEL. A first element 62 on the optics block 20 splits off and collimates part of the beam output by the VCSEL and directs it to the power monitor 14'. A second optical element 64 may be provided on the optics block 20 to focus the light
10 onto the power monitor 14'. Details of such a configuration are set forth in commonly assigned, co-pending U.S. Patent Application Serial No. 09/386,280 entitled "Diffractive Vertical Cavity Surface Emitting Laser Power Monitor and System" the entire contents of which are hereby incorporated by reference for all purposes.

Meanwhile, the undeflected portion of the light travels to a third optical element
15 66, where it is focused onto the fiber, after being reflected by the reflective surface 60. Thus, in accordance with the present invention, alignment may be realized using the alignment holes already available on the fiber housing without requiring that the components all be in the same plane. While a VCSEL array is discussed above, a detector array could be similarly positioned.

20 While all the previous configurations have illustrated the opto-electronic devices bonded on the bottoms thereof to a substrate 16, thereby requiring wire-bonding to

SUBSTITUTE SHEET (RULE 26)

realized their required electrical connections, Figures 6-7B illustrate bonding the top of the opto-electronic devices to the optics block. Since all the interconnections on the typical opto-electronic devices are provided on the top thereof, such bonding allows the use of wire bonding to be eliminated, which in turn allows more compact interconnections to be realized.

As shown in Figure 6, the interconnections to the opto-electronic device 14 can be realized using a pair of flex leads, a signal flex lead 72 and a ground flex lead 74. An interconnect spacer 70 serves the same function as the previous spacer 15, but also includes interconnection tracks for connecting the opto-electronic element 14 to the signal flex lead 72. If space permits, interconnection tracks for the ground flex lead 74 may also be provided on the interconnect spacer 70. Otherwise, the ground flex lead 74 may be attached to the bottom of the opto-electronic device 14, as shown in Figure 6. While shown as a separate element in Figure 6, the interconnect spacer 70 may be integral with the optics block 20. The opto-electronic device preferably is mounted on a heat sink block 78. Thus, the module can be surface mounted or plugged into an electrical interface, e.g., a printed circuit board or flex circuit, without additional housing which may be needed to connect the wire bond configurations discussed above.

As shown in Figures 7A and 7B, another configuration eliminating the need for wire bonds includes again providing the interconnect spacer 70 to which the opto-electronic device 14, here a VCSEL array, is bonded. While shown as a separate element in Figures 7A and 7B, the interconnect spacer 70 may be integral with the optics block

-15-

20. Instead, of connecting the opto-electronic device 14 to flex leads, the interconnect spacer 70 now includes metal lines 80 on a bottom surface thereof, extending from the opto-electronic device 14. A chip carrier 86, preferably ceramic, has a hole 84 therein for receiving the opto-electronic device 14 therein. The chip carrier 86 is preferably attached to the spacer 80 using a sealing ring 88, e.g., any conventional adhesive.

The chip carrier 86 also includes a connection region 82 with vias to connect the metal lines 80 to the outside, e.g., through a bottom surface of the chip carrier. This may be accomplished, for example, using holes 90 through the chip carrier lined with metal. Thus, the module can be surface mounted or plugged into an electrical interface, e.g., a printed circuit board or flex circuit, without additional housing which may be needed to connect the wire bond configurations discussed above. While the configuration shown in Figures 7A and 7B has assumed all required connections for the opto-electronic device are on the top surface thereof, a ground connection could also be provided on the bottom surface.

When using a spacer which is transparent to wavelengths of interest and in the path of the radiation, such as shown in Figures 6-7B, the spacer may have optical elements formed thereon. For example, if the spacer and the optics block are made of the same material, there will not be an optical interface between them. Thus, a bottom surface of the spacer can be used on a second optical surface. The opto-electronic device could be slightly removed from this bottom surface even when bonded to the bottom surface, for example, providing a thick enough layer of bonding material. If a separate spacer is not

SUBSTITUTE SHEET (RULE 26)

-16-

used, the opto-electronic device may still be attached to the bottom of the optics block with this bonding spacing such that the optics block still provides two surfaces. If the spacer and the optics block are made of different material, optics may be provided on either surface of the spacer. Of course, additional optics block may be bonded together to provide the surfaces needed, but with a commensurate increase in thickness of the system.

It is further noted that any of the individual components described in connection with a particular embodiment may be used with other configurations. For example, the opto-electronic device 14 as shown in Figures 6-7B, may be bonded to the bottom of the optics block in the configuration of Figures 1-2A.

While the present invention is described herein with reference to illustrative embodiments for particular applications, it should be understood that the present invention is not limited thereto. Those having ordinary skill in the art and access to the teachings provided herein will recognize additional modifications, applications, and embodiments within the scope thereof and additional fields in which the invention would be of significant utility without undue experimentation. Thus, the scope of the invention should be determined by appended claims and their legal equivalents, rather than by examples given.

What is claimed is:

- 1 1. An integrated active optical system comprising:
2 an opto-electronic device;
3 an optics block; and
4 a spacer, separate from said optics block, providing spacing between said opto-
5 electronic device and said optics block, said opto-electronic device, said optics block and
6 said spacer being aligned and bonded together.
- 1 2. The system of claim 1, wherein the spacer and the optics block are aligned and
2 bonded on a wafer level prior to dicing.
- 1 3. The system of claim 1, wherein the spacer surrounds the opto-electronic device.
- 1 4. The system of claim 1, further comprising a substrate, both a bottom of said
2 opto-electronic device and the spacer being bonded to the substrate.
- 1 5. The system of claim 4, wherein the opto-electronic device is bonded to the
2 substrate on a wafer level prior to dicing.
- 1 6. The system of claim 1, wherein a top surface of said opto-electronic device is
2 bonded to the spacer and the spacer further comprises interconnection tracks.

1 7. The system of claim 6, wherein the opto-electronic device is bonded to the
2 spacer on a wafer level prior to dicing.

1 8. The system of claim 6, wherein the substrate, the optics block and the spacer
2 are all made of silicon.

1 9. The system of claim 1, wherein the system is surface mounted to an electrical
2 interface.

1 10. The system of claim 1, wherein said opto-electronic device comprises at least
2 two opto-electronic devices.

1 11. The system of claim 10, wherein said at least two opto-electronic devices
2 comprise a light source and a light detector.

1 12. The system of claim 10, wherein said at least two opto-electronic devices
2 comprise an array of identical opto-electronic devices.

1 13. A system comprising:
2 a housing having a fiber;

3 an opto-electronic device, and

4 an optics block having two surfaces, each surface having an optical element
5 thereon, said optics block coupling light between the opto-electronic device and the fiber,
6 the housing, the opto-electronic device and the optics block being integrated together.

1 14. The system of claim 13, wherein said opto-electronic device comprises at least
2 two opto-electronic devices and the fiber comprises at least two fibers.

1 15. The system of claim 14, wherein said at least two opto-electronic devices are
2 separated from each other in at least one direction by more than said at least two fibers
3 are separated from one another.

1 16. The system of claim 14, wherein said at least two opto-electronic devices are
2 separated from each other in at least two directions by more than said at least two fibers
3 are separated from one another in each respective direction.

1 17. The system of claim 14, wherein said at least two opto-electronic devices
2 comprise a light source and a light detector.

1 18. The system of claim 14, wherein said at least two opto-electronic devices
2 comprise an array of identical opto-electronic devices.

1 19. The system of claim 13, further comprising a spacer block between the optics
2 block and the opto-electronic device.

1 20. The system of claim 19, further comprising a substrate, both a bottom of said
2 opto-electronic device and the spacer being bonded to the substrate.

1 21. The system of claim 19, wherein a top surface of said opto-electronic device
2 is bonded to the spacer and the spacer further comprises interconnection tracks.

1 22. The system of claim 13, wherein an optical axis of the fiber and an optical axis
2 of the opto-electronic device are at an angle.

1 23. The system of claim 22, further comprising a reflective surface directing light
2 between the opto-electronic device and the fiber.

1 24. The system of claim 13, wherein said at least one optical element on the optics
2 block homogenizes light.

1 25. The system of claim 13, wherein the system is surface mounted to an electrical
2 interface.

1 26. The system of claim 13, wherein the optical elements on the optics block are
2 made on a wafer level.

1 27. A system comprising:
2 an opto-electronic device;
3 an optics block having at least one optical element thereon; and
4 a sealing structure surrounding the opto-electronic device, the opto-electronic
5 device, the optics block and the sealing structure being integrated.

1 28. The system of claim 27, wherein the system is surface mounted to an electrical
2 interface.

1 29. The system of claim 27, further comprising a spacer between the optics block
2 and the opto-electronic device.

1 30. The system of claim 29, wherein the sealing structure comprises a bottom
2 surface of the optics block and the spacer.

1 31. The system of claim 29, wherein an optical element is provided on the spacer.

-22-

1 32. The system of claim 27, further comprising a substrate receiving the opto-
2 electronic device therein.

1 33. The system of claim 32, wherein the sealing structure comprises the substrate
2 and a bottom surface of the optics block.

1 34. The system of claim 32, further comprising a spacer block between the optics
2 block and the opto-electronic device.

1 35. The system of claim 34, wherein the sealing structure comprises the substrate
2 and a bottom surface of the spacer.

1 36. The system of claim 32, further comprising vias through the substrate
2 connecting the opto-electronic device to other systems.

1 37. The system of claim 32, wherein the opto-electronic device is in contact with
2 the substrate, the substrate serving as a heat sink for the opto-electronic device.

FIG. 1A

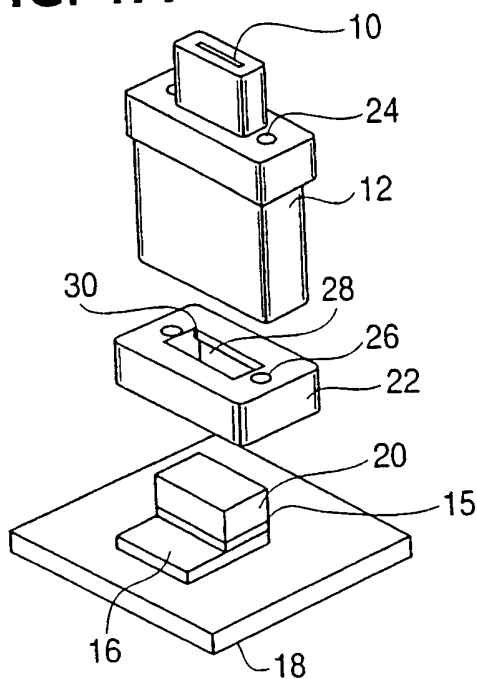


FIG. 1B

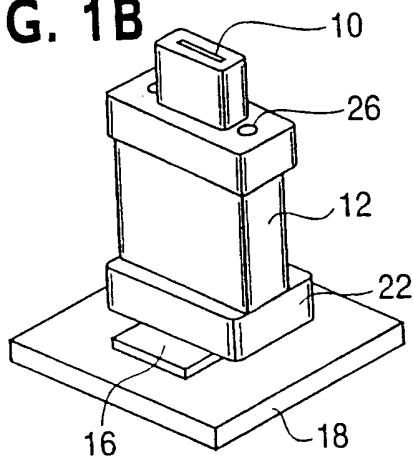


FIG. 1C

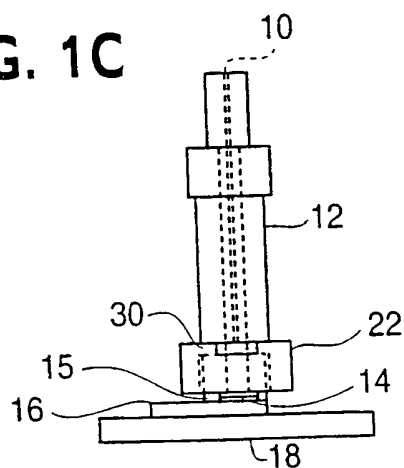


FIG. 1D

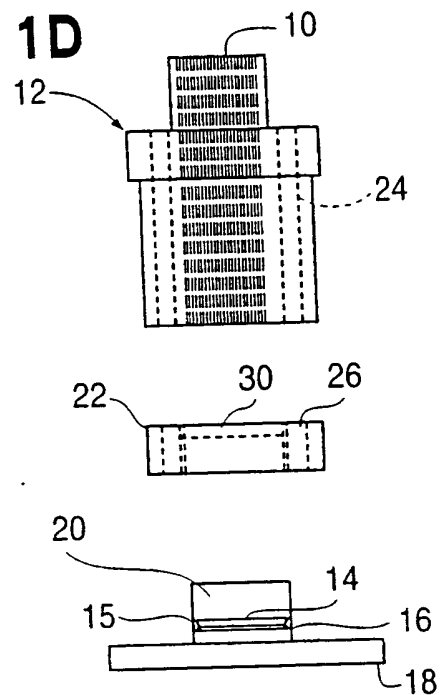


FIG. 1E

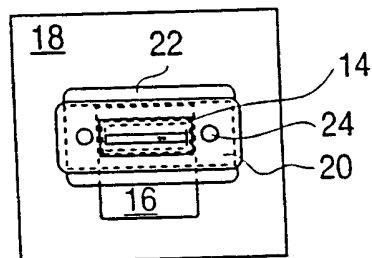


FIG. 1F

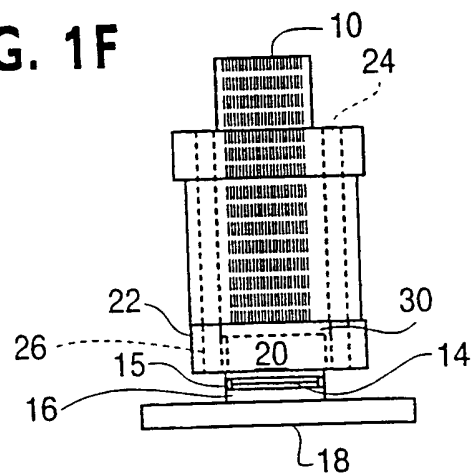


FIG. 2A

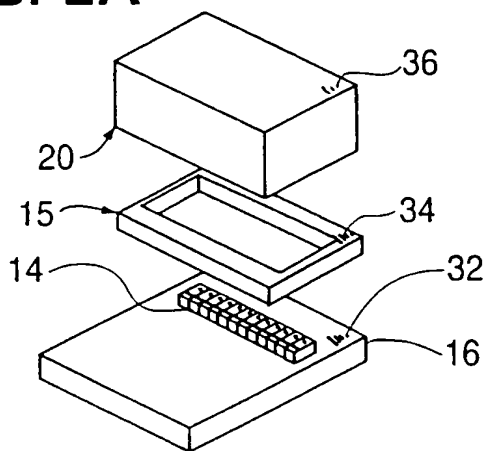


FIG. 2B

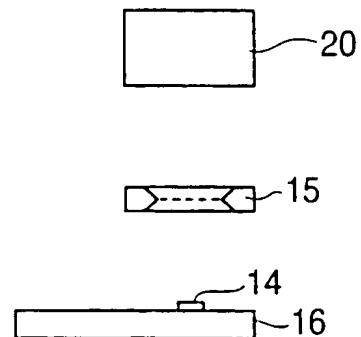


FIG. 3A

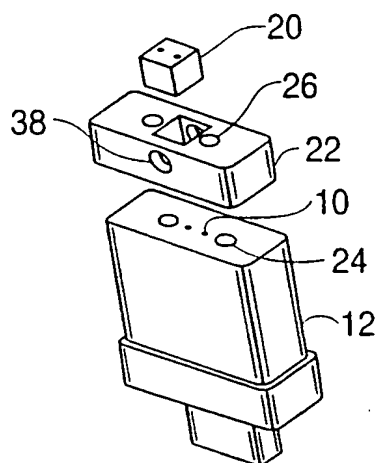


FIG. 3B

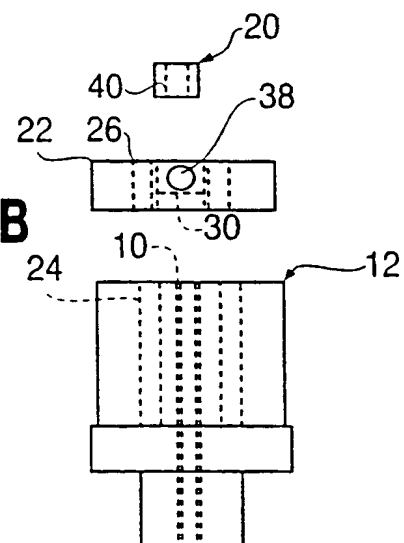


FIG. 4A

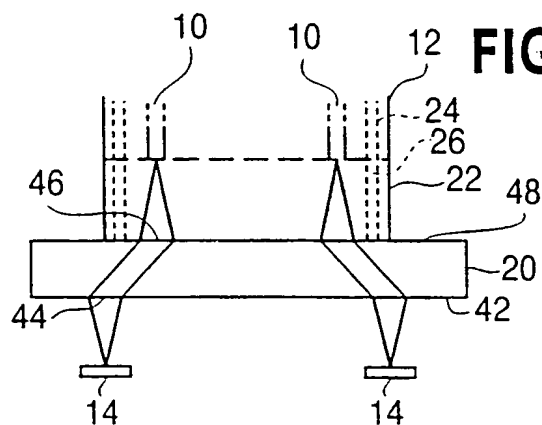
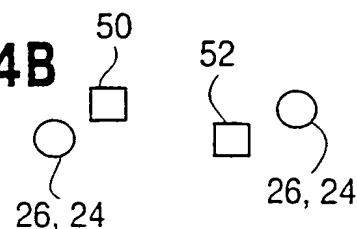


FIG. 4B



2/3

FIG. 5

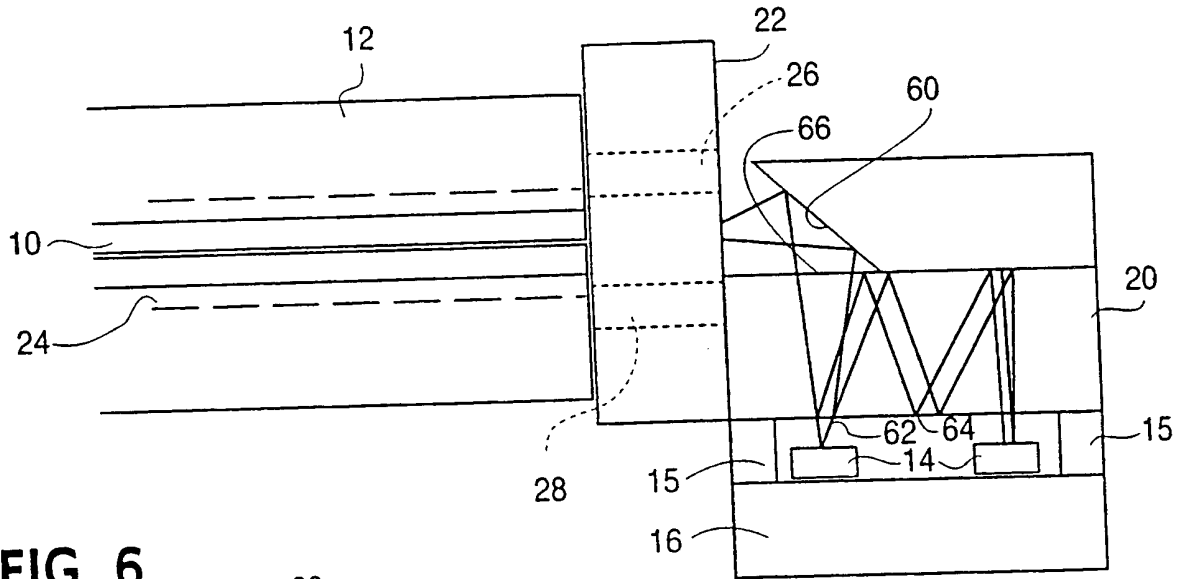


FIG. 6

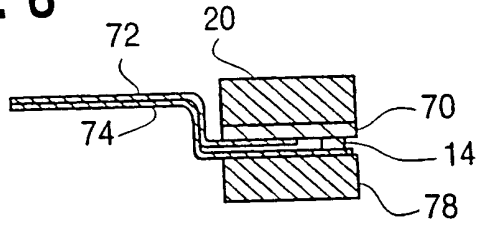


FIG. 7A

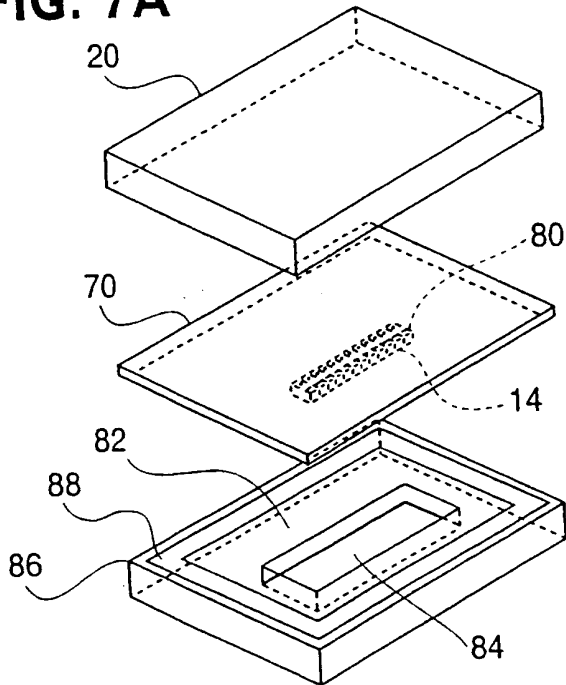
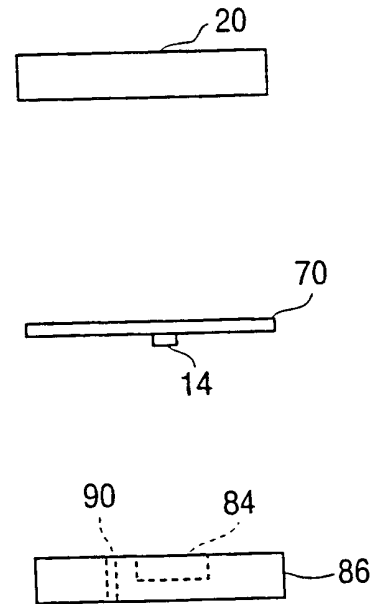


FIG. 7B



INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 00/28086

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G02B6/42

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G02B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	SATAKE T ET AL: "MT MULTIFIBER CONNECTORS AND NEW APPLICATIONS" PROCEEDINGS OF THE ELECTRONIC COMPONENTS AND TECHNOLOGY CONFERENCE, US, NEW YORK, IEEE, vol. CONF. 44, 1 May 1994 (1994-05-01), pages 994-999, XP000479208 ISBN: 0-7803-0914-6 abstract; figures 1-4, 15	1, 13, 27
A	DE 197 42 895 A (SIEMENS) 8 April 1999 (1999-04-08) abstract; figures 1-9	1, 13, 27

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- *Z* document member of the same patent family

Date of the actual completion of the international search

25 January 2001

Date of mailing of the international search report

02/02/2001

Name and mailing address of the ISA
European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Malic, K

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/28086

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
DE 19742895 A	08-04-1999	WO 9915927 A EP 1018052 A	01-04-1999 12-07-2000